

PATENT  
450100-03272**REMARKS**

The Office Action in the above-identified application has been carefully considered and this amendment has been presented to place this application in condition for allowance. Accordingly, reexamination and reconsideration of this application are respectfully requested.

Claims 1, 3 and 5-7 are in the present application. It is submitted that these claims, are patentably distinct over the prior art cited by the Examiner, and that these claims are in full compliance with the requirements of 35 U.S.C. § 112. Changes to the claims, as presented herein, are not made for the purpose of patentability within the meaning of 35 U.S.C. sections 101, 102, 103 or 112. Rather, these changes are made simply for clarification and to round out the scope of protection to which Applicants are entitled. Claim 2 is canceled.

Claims 1-3 and 5-7 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Agrawal et al. (U.S. Patent 6,363,108) in view of Fukasawa et al. (U.S. Patent 5,920,591). However, the present claims now recite "said synchronicity detection apparatus detects a local maximum correlation value over a searchable range equal to said  $m$  shift registers; the searchable range being repeated plural times over the length of the spread code." (Claim 1) This distinguishing feature of the present invention is shown in Figure 10 and can be directly contrasted with the features of prior art systems shown in Figure 3. As shown in Figures 3 and 10, both the present invention and prior art systems perform a number of correlations ( $n \times m$ ) over an entire spread code cycle ( $T$ ). However, prior art systems correlate over a searchable range (of  $n \times m$  chips) for the entire spread code cycle; whereas the present invention repeats (e.g.  $n$  times) over a reduced searchable range ( $= m$  chips) in the spread code cycle. As a result,

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the present invention detects a local maximum value correlation over each reduced searchable range; rather than detecting a single maximum value for the entire spread code cycle as in the prior art. In this manner, the present invention accomplishes its objective of detecting a plurality of correlations within one spread code cycle. Applicants respectfully assert that Agrawal and Fukasawa are both directed to systems analogous to the prior art system shown in Figure 3. Accordingly, Agrawal and Fukasawa fail to meet this limitation and the rejected claims should now be allowed.

In view of the foregoing amendment and remarks, it is respectfully submitted that the application as now presented is in condition for allowance. Early and favorable reconsideration of the application are respectfully requested.

No additional fees are deemed to be required for the filing of this amendment, but if such are, the Examiner is hereby authorized to charge any insufficient fees or credit any overpayment associated with the above-identified application to Deposit Account No. 50-0320.

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If any issues remain, or if the Examiner has any further suggestions, he/she is invited to call the undersigned at the telephone number provided below. The Examiner's consideration of this matter is gratefully acknowledged.

Respectfully submitted,  
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By: \_\_\_\_\_

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